

FIG. 1

FIG. 2 is a block diagram of a system architecture showing two applications, Application #1 and Application #2, connected to a common IEEE 1394 Bus. The diagram illustrates the internal components of each application and their connection to the bus.

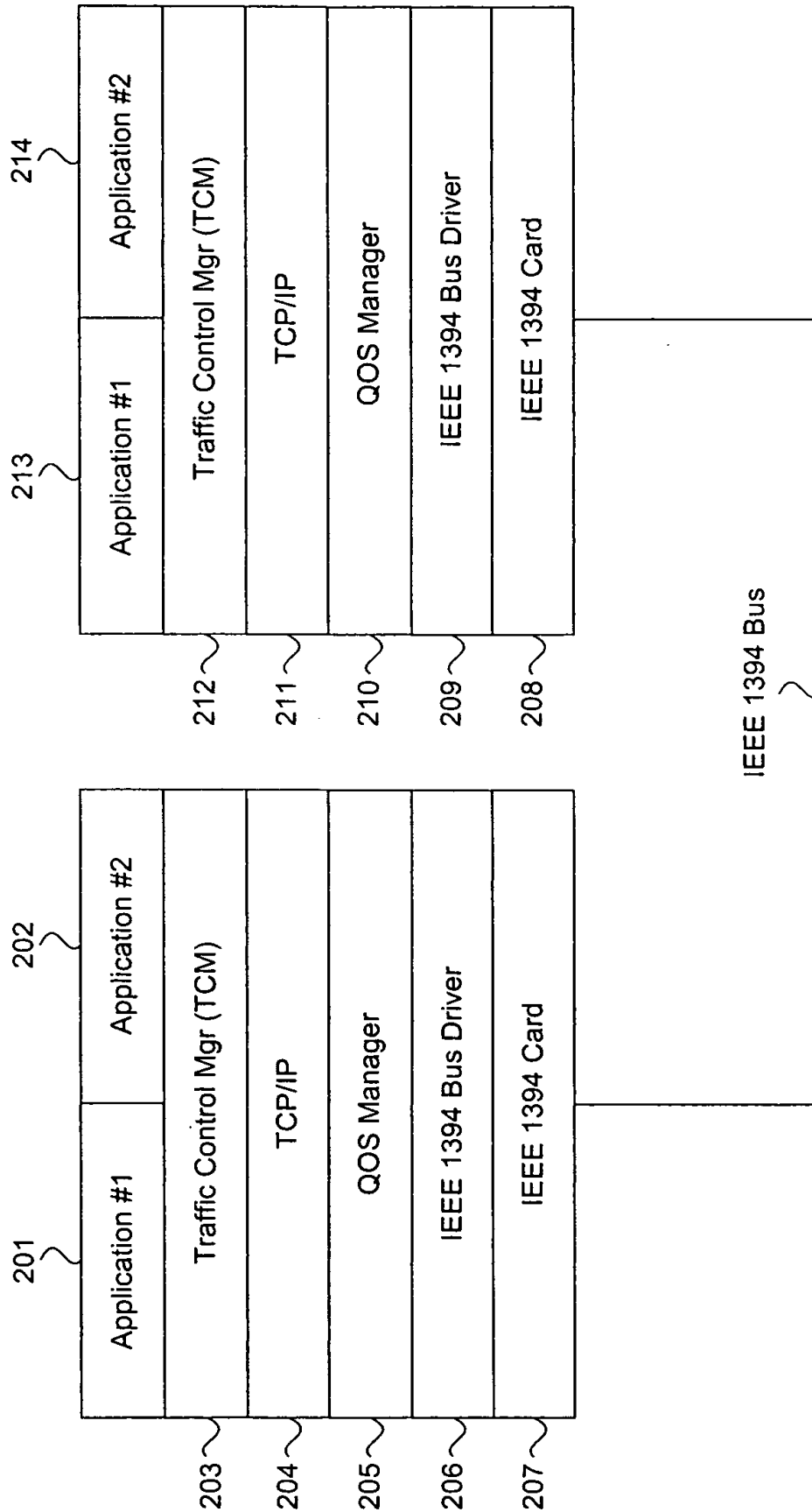


FIG. 2

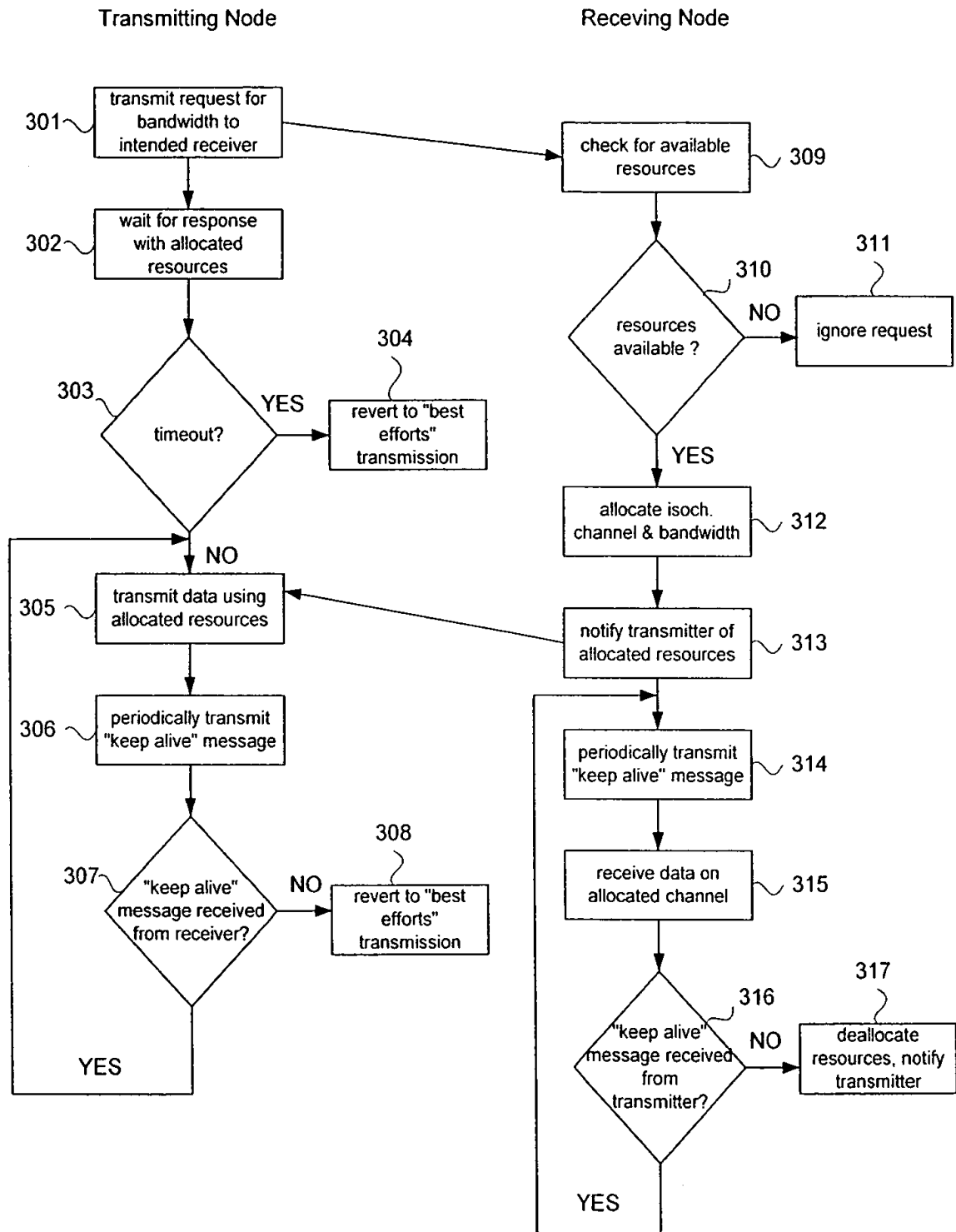


FIG. 3

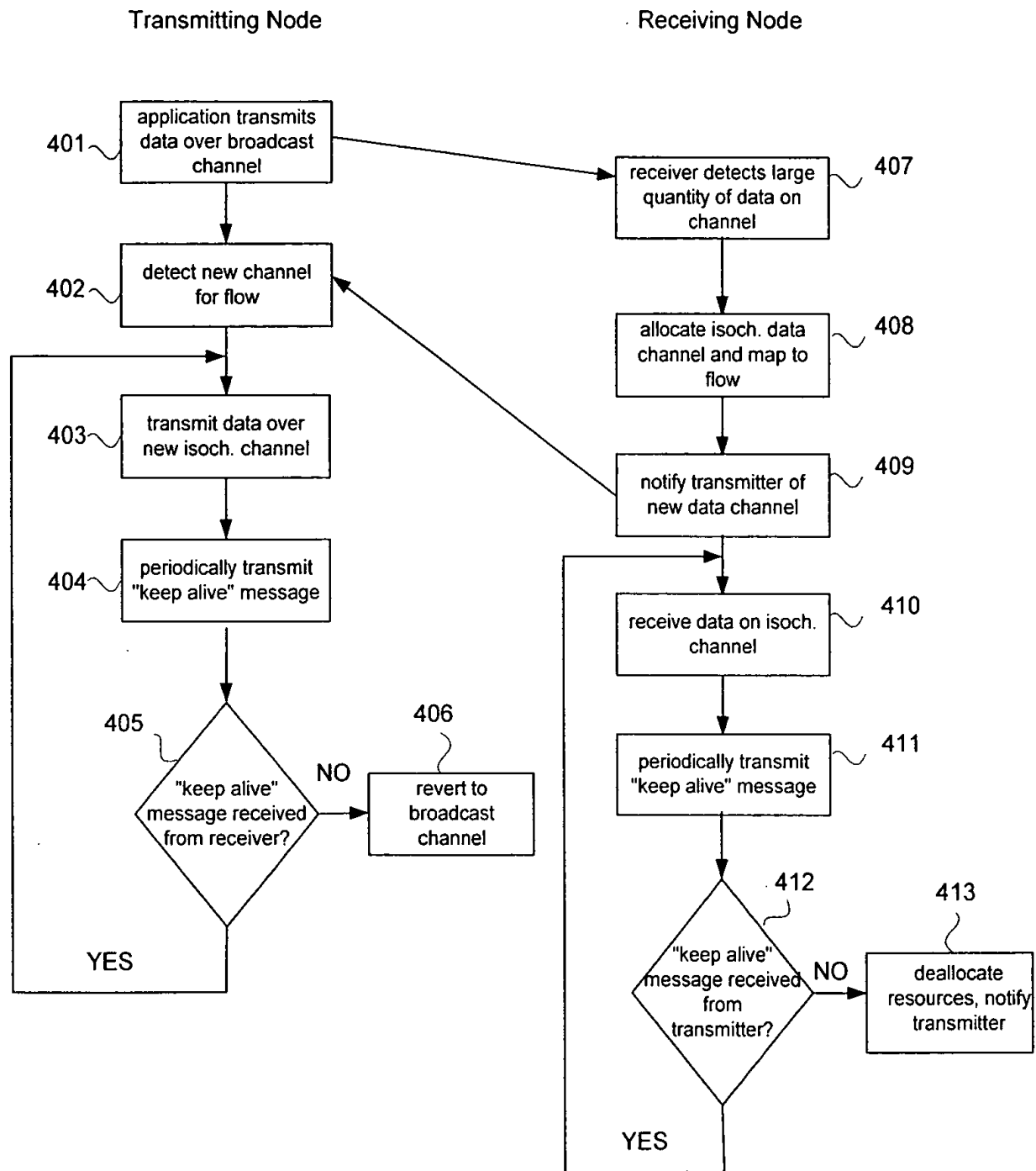


FIG. 4

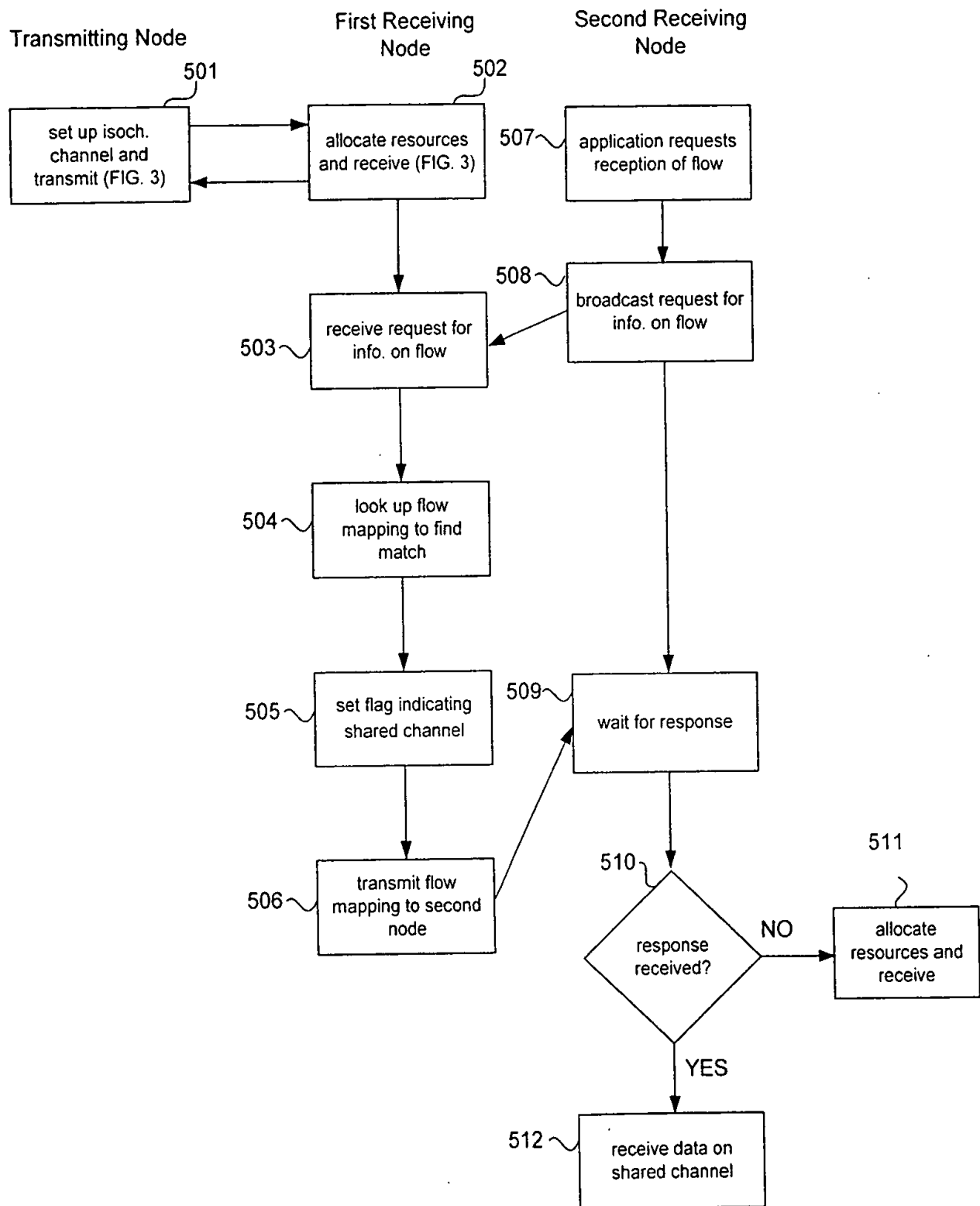


FIG. 5